

PLC

PGM-500



FARA N-PLUS, SPC
Handy Loader(PGM500)





1.	1.1 PGM-500
	1.2
	1.3
2.	2.1
	2.2
3.	3.1
	3.2
4.	4.1 (EDIT).....
	4.2 (MONITOR).....
	4.3 (SYSTEM).....
5.	5.1
	5.2
	5.3 Timer/Counter/SR
	5.4
	5.5
	5.6
6.	6.1
	6.2
	6.3
	6.4
	6.5
	6.6
	6.7
	6.8 SCAN RUN
	6.9 PLC
7.	7.1 PLC - (Inform).....
	7.2 PLC - (Program).....
	7.3 PLC - (Control).....
	7.4 PLC - (Config).....
	7.5 PLC - (Check).....
	7.6 - (Inform).....
	7.7 - (Program).....
	7.8 PACK - (Inform).....
	7.9 PACK - (Program).....
	7.10 PGM - (Inform).....
	7.11 PGM - (Program).....
	7.12 PGM - (Control).....
8.	8.1
	8.2
	A.1
	A.2

HANDY PROGRAMMER PGM-500

「N PLUS」 「SPC-」, 「SINGLE A」
(TOOL)
가

「3.」
「5.」
「8.」
「」

ENTER

ESC

ON-LINE

PLC

10

PLC 가 가 , 가

1

1.1	PGM-500
1.2
1.3

BACKLIGHT LCD

30,000 LED BACKLIGHT
가 .

OFF-LINE

가 , PLC
가 . PLC OFF-LINE
가

RS-232C , (Multi-Drop)
RS485 가 . Baud Rate
4.8K, 9.6K, 19.2K, 38.4K bps

OFF-LINE PLC 가

PLC

PLC가 PLC
가 .

1.3

1. .
- 가 가
2. .
3. .
4. PGM-WR
가 .

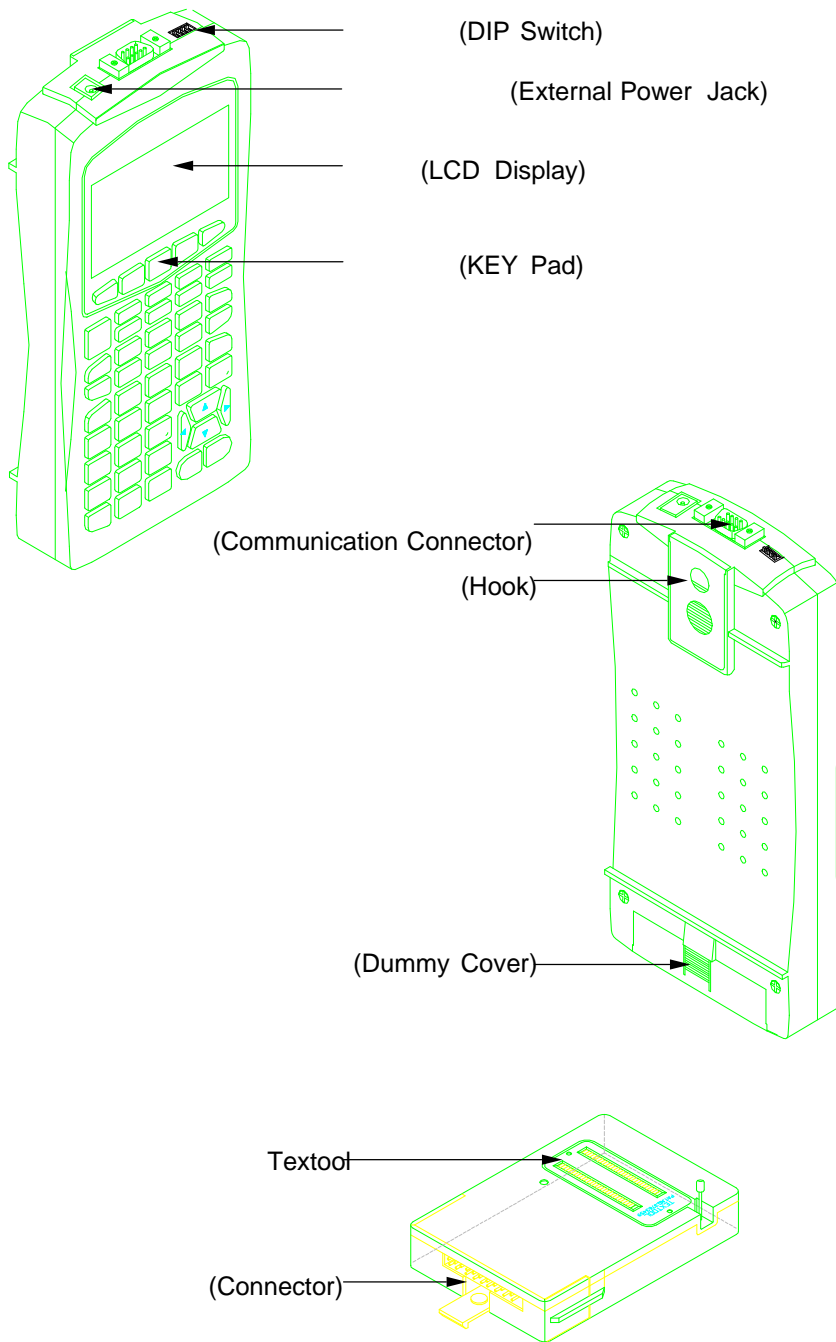
PLC CPU , , ,

2

- 2.1
 -
 -
- 2.2

		0 ~ 40
		-20 ~ 60
		35 ~ 85 %RH
	PGM-500	350 mA(Typ) (in 5 Vdc) (Max:430mA, Min:250mA)
	PGM-WR	50mA
		MEMORY PACK (PH29EE512 150-3CF, SST)
		99mm × 208mm × 30mm (가 × ×)
		350 g

	SPC-10 SPC-24S SPC-100 SPC-120S SPC-300 A-200 N-70PLUS N-700PLUS
	PLC
	Dot-Matrix LCD (128×64) LED Backlight (On/Off 가)
	49 (Beep)
	RS-485/ RS-232C (4.8/9.6/19.2/38.4 Kbps)
	DC 5V ~ 25V (500mA)
	PGM-500 (1 SET) (1) Cable (1 EA) PGM-WR



[2-1]

- 1.
- 2.
- 3.
- 4.

A/S

가

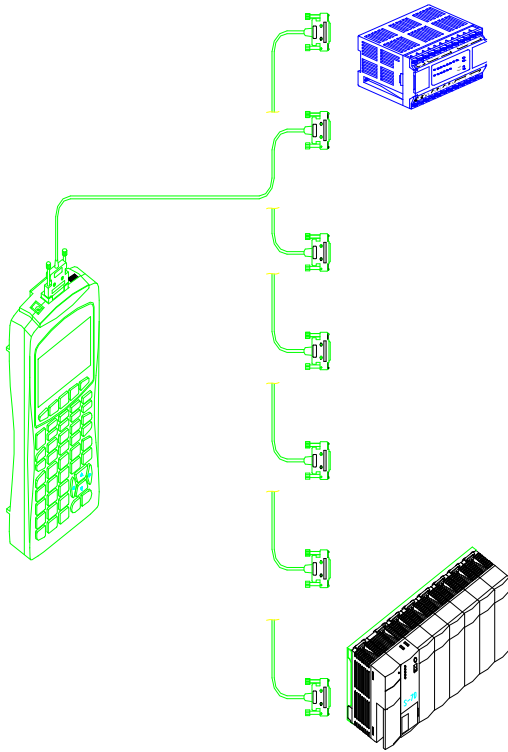
가

3

3.1

3.2

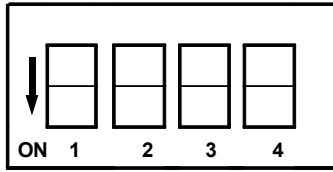
3.1 PGM-500



DIP

PLC PGM-500

DIP

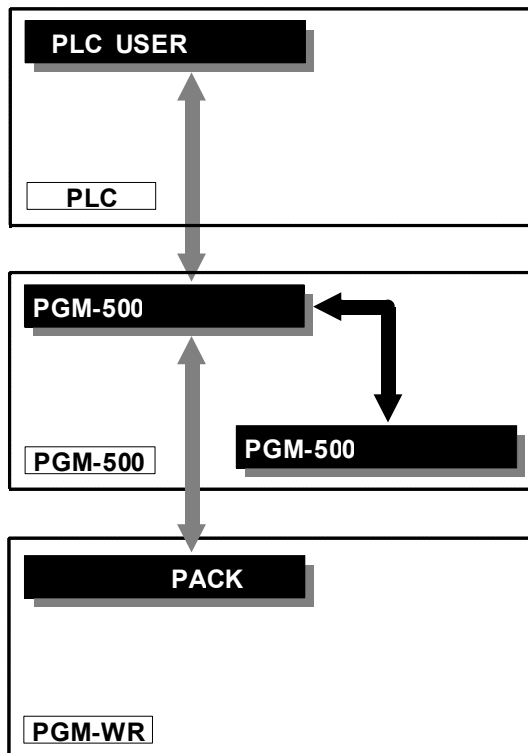


	SW1	SW2	SW3	SW4	Baud Rate
1	OFF	OFF	OFF	OFF	4800 bps
2	ON	OFF	OFF	OFF	9600 bps
3	OFF	ON	OFF	OFF	19200 bps
4	ON	ON	OFF	OFF	38400 bps

FLOW

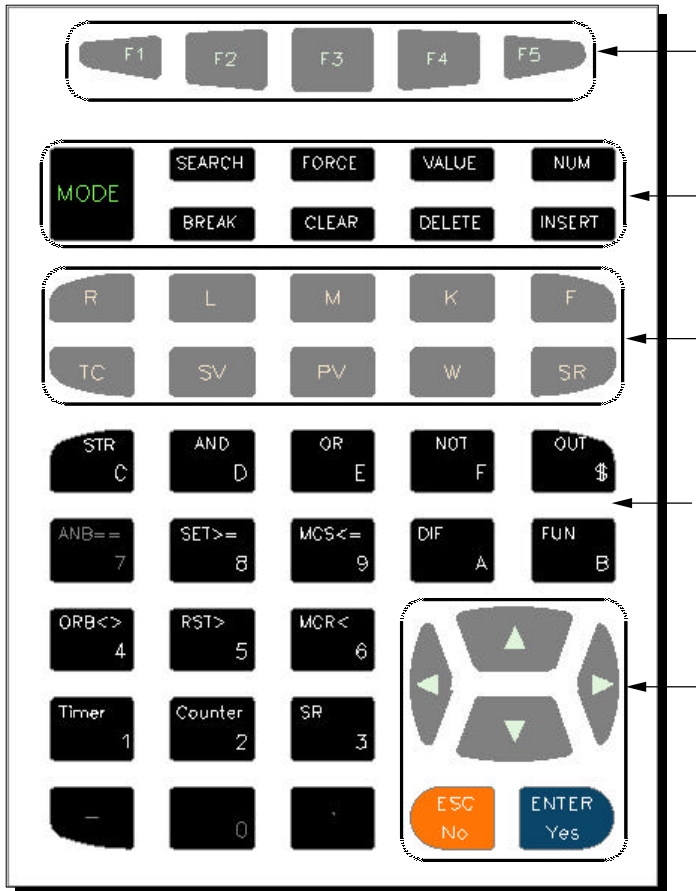
PLC PGM-500 , PGM-WR

PGM-500



[3-2]

3.2 KEY



[3-3]

	F1	F2	F3	F4	F5
PLC (ESC)	RUN		NET		STP
(SEARCH)	T/C	BIT	WRD	NUM	GO#
(VALUE)	BIT		WRD		DBL

[]

KEY	NAME	
MODE		(Toggle)
SEARCH		/ / ,
FORCE		
VALUE		
NUM	Hex Dec	Decimal Hexadecimal (Toggle)
BREAK	Scan Run	One Scan Run (PAUSE REMOTE)
CLEAR	Clear	all clear,
DELETE		,
INSERT		, 가

KEY									
		10	24S	100	120S	300	A200	N-70 PLUS	N-700 PLUS
R		0 ~ 29				0 ~ 127			
L		X				0 ~ 255			
M		0 ~ 31				0 ~ 127			
K		0 ~ 15				0 ~ 127			
F		0 ~ 1				0 ~ 15			
TC	Coil /	0 ~ 255				0 ~ 255			
SV		0 ~ 255				0 ~ 255			
PV		0 ~ 255				0 ~ 255			
W		0 ~ 255				0 ~ 2047			
SR		0 ~ 255				0 ~ 511			

()

KEY		KEY	
STR C	C (, 16)	ORB <> 4	ORB , (.)
AND D	AND D (, 16), Double	RST > 5	RST , ()
OR E	OR E (, 16)	MCR < 6	MCR , ()
NOT F	NOT F (, 16)	TIMER 1	Timer 1
OUT \$	OUT 16 \$	COUNTER 2	Counter 2
ANB == 7	ANB , ()	SR 3	Shift Register 3
SET >= 8	SET , ()	-	
MCS <= 9	MCS , ()	0	0
DIF A	DIF A (, 16)	.	/
FUN B	B		

(/ /)

KEY	NAME	
	Up Arrow	()
	Down Arrow	()
	Left Arrow	
	Right Arrow	
ESC No	ESC No	,
ENTER Yes	ENTER Yes	

4

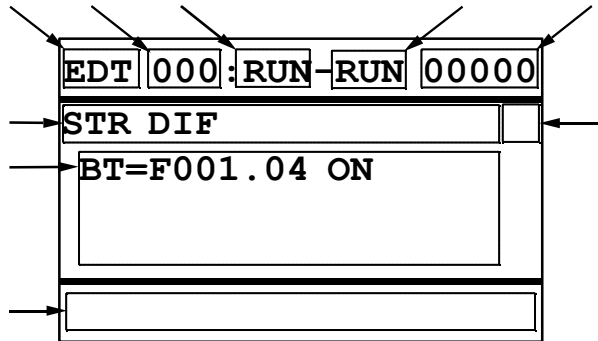
4.1 EDIT
- ON-LINE
- OFF-LINE
4.2 MONITOR
4.3 SYSTEM
- ONLINE
- OFFLINE
- SYSTEM MODE

PGM-500 3가 가
 EDT(Edit), MON(Monitoring), SYS(System) , EDT MON
 [MODE] , EDT SYS, MON SYS [ESC No] [MODE]
 , MON . PLC
 EDIT

4-1 (EDIT)

PLC , 가 가
 PLC Up Load On-line
 Off-line

ON-LINE

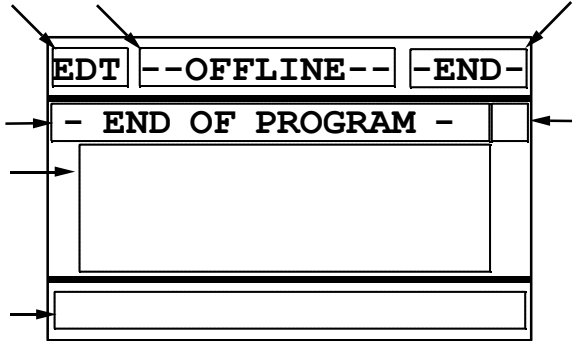


					/ /
	CPU ID				
	PLC		/		

- 1.
2. 가/ / / (消去)
3. Operand /
- 4.
- 5.
6. 10 16
7. SCAN RUN (「6.8 SCAN RUN」)
8. PLC (On/Off/PLC)

[] PLC
 , READY .
 [] 6 .

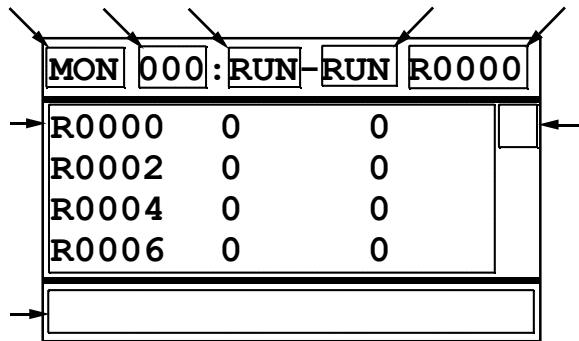
OFF-LINE



	OFF - LINE				
			/		

- 1.
2. 가 / / (消去)
3. Operand /
4. PLC

[] 6 .

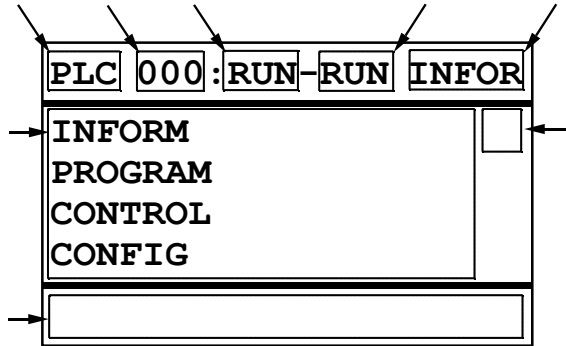


	CPU ID			/
	PLC			

- 1.
2. (WORD)
- 3.
- 4.
5. 10 16
6. SCAN RUN (「6.8 SCAN RUN」)
7. PLC (On/Off/PLC)

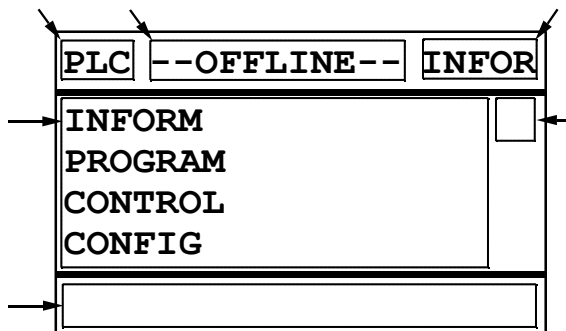
[] 6 .

ON-LINE



	CPU ID				
	PLC				

OFF-LINE



	OFF - LINE				

1. PLC
2. BACKUP
3. OPTION PACK (FLASH)
4. PGM-500

[] 7 .

PLC-CPU	INFORM	- PLC/ CPU/ ROM VERSION - PROGRAM SIZE/ WORD/ STEP - WATCHDOG/ SCAN/ SCAN MAX
	PROGRAM	- UP/ DOWN LOAD - CLEAR
	CONTROL	- CPU STATUS CONTROL - IN/ OUT UPDATE - OUT ENABLE - TIMER INTERRUPT CONTROL - KEEP REGISTER CLEAR CONTROL - PROGRAM BACKUP MONITOR
	CONFIG	- CPU ID/ PASSWORD/ PROGRAM NAME - WATCHDOG/ RTC/ REMOTE/ IN-OUT
	CHECK	- SYSTEM/ SYNTAX CHECK - SYSTEM/ SYNTAX ERROR TABLE
BACKUP	INFORM	- PLC/ CPU/ WORD/ STEP/ NAME
	PROGRAM	- UP/ DOWN LOAD - CLEAR
PACK	INFORM	- PLC/ CPU/ WORD/ STEP/ NAME
	PROGRAM	- UP/ DOWN LOAD - CLEAR
PGM	INFORM	- PLC/ CPU/ WORD/ STEP/ NAME
	PROGRAM	CLEAR, I/O CONFIG
	CONTROL	BAUD RATE BACKLIGHT/BEEP/POWER SAVE ON/OFF

5

5.1

5.2

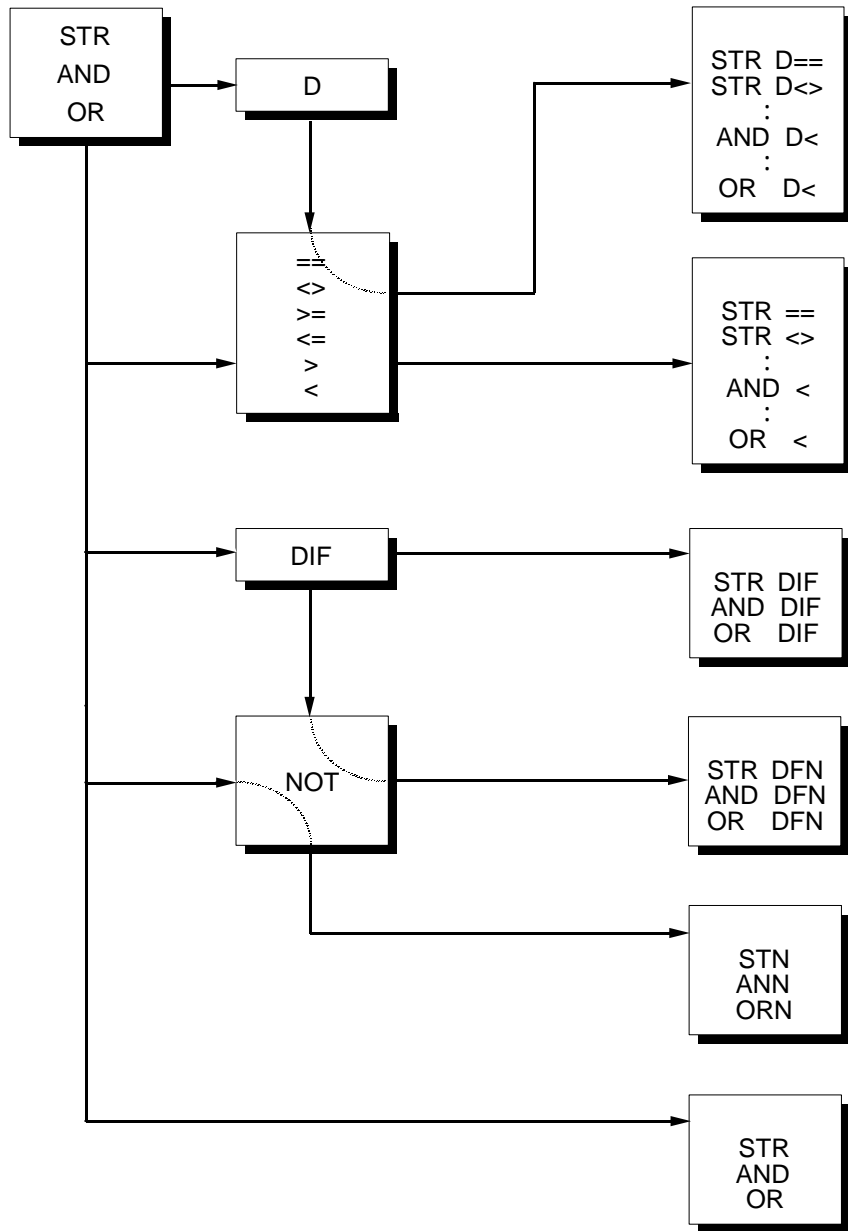
5.3 Timer/Counter/SR

5.4

5.5

5.6

5.1



[5-1]

[] STR, AND, OR DIF, NOT,
 . [5-1]

가

가























가

가

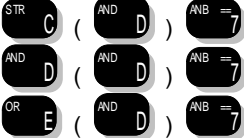
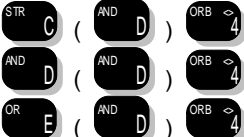
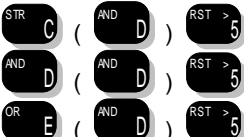
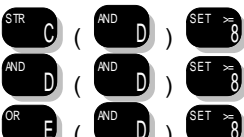
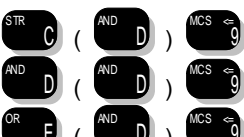
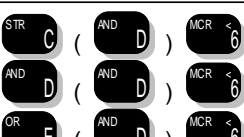


가


.

MNEMONIC			
STR	Start		a
STN	Start Not		b
AND	And		a
ANN	And Not		b
OR	Or		a
ORN	Or Not		b
OUT	Out		
NOT	Not		
STR DIF	Start		
STR DFN	Start Dif. Not		
AND DIF	And Dif.		
AND DFN	And Dif. Not		
OR DIF	Or Dif.		
OR DFN	Or Dif. Not		
ANB	And Block		
ORB	Or Block		
MCS	Master Control Set		
MCR	Master Control Reset		
SET	Set		Set(On)
RST	Reset		Reset(Off)




5.2

MNEMONIC			
STR (D)== AND (D)== OR (D)==	EQUAL		ON
STR (D)<> AND (D)<> OR (D)<>	NOT EQUAL		ON
STR (D)> AND (D)> OR (D)>	GREATER THAN		ON
STR (D)>= AND (D)>= OR (D)>=	GREATER EQUAL		ON
STR (D)<= AND (D)<= OR (D)<=	LESS EQUAL		ON
STR (D)< AND (D)< OR (D)<	LESS THAN		ON

[] D DOUBLE ,



5.3 Timer/Counter/SR

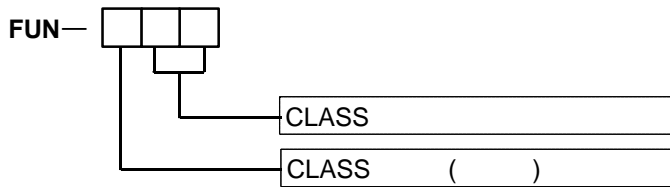
MNEMONIC			
TIM	On Delay Timer		
SST	Single Shot Timer		/
TOF	Off Delay Timer		
UC	Up Counter		
DC	Down Counter		
UDC	Up-Down Counter		/
RCT	Ring Counter		
SR	Shift Register		

[] TIM, SST, TOF TIMER Up Counter, Down Counter, Up-Down Counter, Ring Counter COUNTER

5.4

GROUP	CLASS	
SHIFT	FUN-0XX	Shift
MOVE	FUN-1XX	Block Move
BIT	FUN-2XX	Bit
CONTROL	FUN-3XX	
LOGICAL	FUN-4XX	
ARITHMETIC	FUN-5XX	
CONVERTER	FUN-6XX	Data
BCD	FUN-7XX	BCD
SPECIAL	FUN-8XX	

[] XX 0 ~ 99 가 .



1) CLASS

FUN B + [CODE] + **SEARCH**








2)

FUN B + CODE + **ENTER** Yes






[] “ [] ” CODE .

- ADD

1) CLASS

<p>1) </p>	<p>EDT 000:STP-RUN -END- ????? [FUN-]</p>	<p>- 가</p>
<p>2) </p>	<p>INST. 00008 FUN-0.. SHIFT FUN-1.. MOVE FUN-2.. BIT FUN-3.. CONTROL</p>	<p>- INST. : - 00008 :</p>
<p>3) </p>	<p>INST. 00008 FUN-2.. BIT FUN-3.. CONTROL FUN-4.. LOGICAL FUN-5.. ARITHMETIC</p>	<p>- " FUN-5.. " - 1) " 5 " , SEARCH</p>
<p>4) </p>	<p>INST. [FUN-500] 00008 NEG INC DEC ABS ADD ADC SUB SBC MUL DIV DNEG DINC</p>	<p>- Enter Class 5 Arithmetic - Class 5 , 가 [FUN-500]</p>
<p>5)  </p>	<p>INST. [FUN-504] 00008 NEG INC DEC ABS ADD ADC SUB SBC MUL DIV DNEG DINC</p>	<p>- ADD - 1) " FUN-504 " , 3) SEARCH , ENTER 가 FUN-504</p>
<p>6) </p>	<p>OPERAND 00008 ADD [FUN-504] D = S1= S2=</p>	<p>- Enter ADD - 가 - 5.5</p>

2) CODE

<p>1) </p>	<pre>EDT 000:STP-RUN -END- ????? [FUN-]</pre>	<p>- 가</p>
<p>2)   </p>	<pre>EDT 000:STP-RUN -END- ????? [FUN-504]</pre>	<p>- 가 ADD CODE가 504 504</p>
<p>3) </p>	<pre>OPERAND 00008 ADD [FUN-504] D = S1= S2=</pre>	<p>- Enter ADD - 가 - 5.5</p>

5.5

BIT

Bit bit (dot) , R,
 1, 5 가 R001.05 .

1) Dot

가 .
 R001.5, R01.05, R01.5, R1.05, R1.5

2) Dot

가 .
 R00105, R0105, R105

[] Dot가 bit .
 R15 R000.15 .

[] Timer/Counter Coil 10 가
 .(TC000 ~ TC255)

) +

[] 가 4 .

1) 16

 0 F 16 .

2) 10

0~9 .

5.6

		12345■	
	MCR < 6	123456■	.
		123456	
		123456	.
		123456	
		123457	
	INSERT	12345■7	.
	MCR < 6	1234567	
	DELETE	123456	.
		23456	가
	CLEAR	123456	.
		■	.

[]



6

6-1 가 &

6-2

6-3

6-4

6-5

6-6

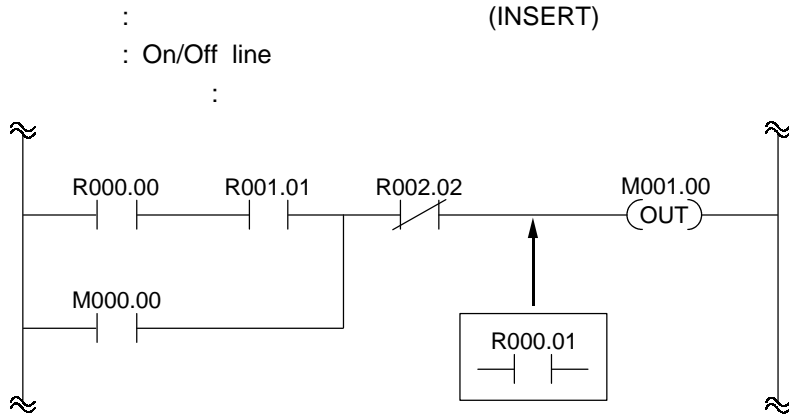
6-7

6-8 Scan Run

6-9 PLC

6.1 가 & (STEP APPEND & INSERT)

(INSERT)



STEP	
:	:
00011	STR R000.00
00012	AND R001.01
00013	OR M000.00
00014	ANN R002.02
00015	OUT M001.00
:	:

STEP	
:	:
00011	STR R000.00
00012	AND R001.01
00013	OR M000.00
00014	ANN R002.02
00015	AND R000.01
00016	OUT M001.00
:	:

[6-1]

SEARCH F5

Timer 1 RST > 5

ENTER Yes

INSERT

EDT 000:RUN-RUN 00015

OUT

BT=M001.00 OFF

EDT 000:RUN-RUN 00015

1)

2) Search F5(Go#)

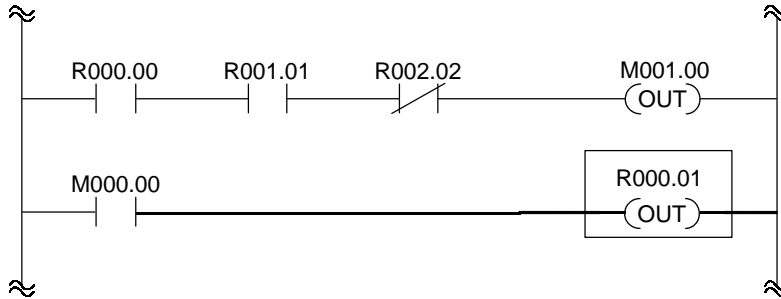
[] 6.5

- Insert

AND D	<p>EDT 000:RUN-RUN 00015</p> <hr/> <p>AND</p> <hr/>	- [] 5.1
ENTER Yes	<p>OPERAND 00015</p> <hr/> <p>AND</p> <p>BT=</p> <hr/>	- Operand 가
R 0 0 0 0 0 Timer 1	<p>OPERAND 00015</p> <hr/> <p>AND</p> <p>BT=R000.01</p> <hr/>	- Operand [] Operand 5.5
ENTER Yes	<p>EDT 000:RUN-RUN 00016</p> <hr/> <p>OUT</p> <p>BT=M001.00 OFF</p> <hr/>	- ENTER Operand 가 가
[]	<p>EDT 000:RUN-RUN 00015</p> <hr/> <p>AND</p> <p>BT=R000.01 OFF</p> <hr/>	[] 가

가(APPEND)

: 가(APPEND, PLC/PGM)
 : On/Off line
 :



STEP	
:	:
00010	STR R000.00
00011	AND R001.01
00012	ANN R002.02
00013	OUT M001.00
00014	STR M000.00
-END-	



STEP	
:	:
00010	STR R000.00
00011	AND R001.01
00012	ANN R002.02
00013	OUT M001.00
00014	STR M000.00
00015	OUT R000.01
-END-	

[6-2] 가

SEARCH

F5

Timer
1

0

0

0

0

ENTER
Yes

-

EDI 000:RUN-RUN -END-

- END OF PROGRAM - ▲

1)
2)

INSERT

or

ENTER
Yes

[] 가 INSERT

EDT 000:RUN-RUN -END-

ENTER 가

OUT \$

OPERAND 00015

OUT

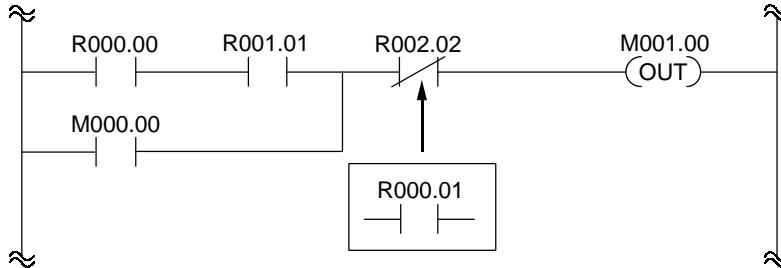
BT=■

- Operand 가

R	0	0	<table border="1"> <tr> <td>OPERAND</td> <td>00015</td> </tr> <tr> <td>OUT</td> <td>BT=R000.01</td> </tr> </table>	OPERAND	00015	OUT	BT=R000.01	- Operand . [] Operand 5.5
OPERAND	00015							
OUT	BT=R000.01							
.	0	Timer 1	<table border="1"> <tr> <td>EDT 000:RUN-RUN -END-</td> <td></td> </tr> <tr> <td>- END OF PROGRAM - ▲</td> <td></td> </tr> </table>	EDT 000:RUN-RUN -END-		- END OF PROGRAM - ▲		- ENTER Operand .
EDT 000:RUN-RUN -END-								
- END OF PROGRAM - ▲								

6.2 (STEP CHANGE)

:
: On/Off line 가



STEP		STEP	
:	:	:	:
00010	STR R000.00	00010	STR R000.00
00011	AND R001.01	00011	AND R001.01
00012	OR M000.00	00012	OR M000.00
00013	ANN R002.02	00013	AND R000.01
00014	OUT M001.00	00014	OUT M001.00
:	:	:	:

[6-3]

SEARCH

F5

Timer 1

SR 3

ENTER Yes

EDT 000:RUN-RUN 00013
 ANN
 BT=R002.02 OFF

-
 1)
 2) Search F5(Go#)

ENTER Yes

EDT 000:RUN-RUN 00013
 ANN
 BT=R002.02 OFF

- ENTER

AND D

EDT 000:RUN-RUN 00013
 AND
 BT=R002.02 OFF

-

[] 6.5

ENTER Yes

OPERAND	00013
AND	BT=R002.02

- [] ENTER

R

0

0

0

.

0

Timer 1

OPERAND	00013
AND	BT=R000.01

- Operand []Operand 5.5 5.6

ENTER Yes

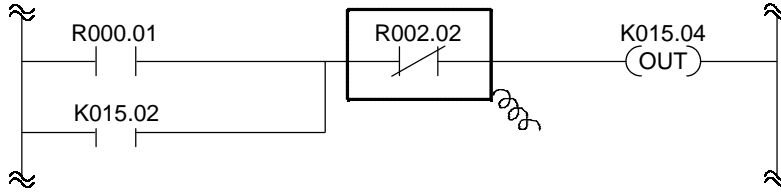
EDT 000:RUN-RUN	00013
AND	BT=R000.01 OFF

-

6.3 (STEP DELETE)

: (DELETE, PLC/PGM)

: On/Off line



STEP		STEP	
:	:	:	:
00010	STR R000.01	00010	STR R000.01
00011	OR K015.02	00011	OR K015.02
00012	ANN R002.02	00012	OUT K015.04
00013	OUT K015.04	:	:
:	:	:	:

[6-4]

SEARCH F5

Timer 1 Counter 2

ENTER Yes

DELETE

EDT 000:RUN-RUN 00012

ANN BT=R002.02 OFF

EDT 000:RUN-RUN 00012

OUT BT=K015.04 OFF

-

1)

2) Search F5(Go#)

[] 6.5

- DELETE


6.4

(PROGRAM ALL CLEAR)

: PROGRAM ALL CLEAR
 : On/Off line
 :

STEP			STEP	
:		:	-END-	- END OF PROGRAM -
00010	STR	R000.00		
00011	AND	R001.01		
00012	OR	M000.00		
00013	ANN	R002.02		
00014	OUT	M001.00		
:		:		

[6-5] Program all Clear




EDT 000:RUN-RUN 00012

ANN
BT=R002.02 OFF

CLEAR PLC? [Y/N]

- CLEAR

[] ESC




EDT 000:RUN-RUN 00012

ANN
CLEAR PROGRAM ...

CLEAR MEMORY? [Y/N]

· ENTER PLC


· PGM-500



EDT 000:RUN-STP -END-

- END OF PROGRAM -

· ENTER PGM



EDT 000:READY 00012

ANN
BT=R002.02 OFF

· ESC PGM



· READY PLC PGM

6.5 (SEARCH)




- : 1. T/C CHANNEL
 - 2. BIT/ WORD
 - 3. NUMBER
 - 4. GOTO STEP
- : On/Off line
- []

T/C CHANNEL

SEARCH	<div style="border: 1px solid black; padding: 5px;"> <p>EDT 000:RUN-RUN 00001</p> <p>STR DIF ▼</p> <p style="text-align: center;">SEARCH</p> <p style="text-align: center;">T/C BIT WRD NUM GO#</p> </div>	- SEARCH .. 가 가
F1	<div style="border: 1px solid black; padding: 5px;"> <p>EDT 000:RUN-RUN 00001</p> <p>STR DIF ▼</p> <p style="text-align: center;">SEARCH</p> <p>T/C : ■</p> </div>	- T/C F1
Counter 2 RST > 5	<div style="border: 1px solid black; padding: 5px;"> <p>EDT 000:RUN-RUN 00001</p> <p>STR DIF ▼</p> <p style="text-align: center;">SEARCH</p> <p>T/C : 25 ■</p> </div>	- T/C
ENTER Yes	<div style="border: 1px solid black; padding: 5px;"> <p>SEARCH 00011</p> <p>TIM ▲▼</p> <p>CH=25</p> <p>SV=70</p> <p>REPEAT SEARCH? [Y/N]</p> </div>	- STEP 가 " REPEAT SEARCH?" YES ENTER NO ESC

	<p>SEARCH 00011</p> <hr/> <p>TIM ▶</p> <p>CH=25</p> <p>SV=70</p> <hr/> <p>NOT FOUND ITEM</p>	<p>- " NOT FOUND ITEM "</p> <p>" REPEAT SEARCH? "</p> <p>YES</p>
	<p>EDT 000:RUN-RUN 00011</p> <hr/> <p>TIM ▶</p> <p>CH=25 0</p> <p>SV=70 70</p>	<p>-</p> <p>- " REPEAT SEARCH? "</p> <p>NO</p>

BIT & WORD

	<p>EDT 000:RUN-RUN 00001</p> <hr/> <p>STR DIF ▶</p> <div style="border: 1px solid black; padding: 2px; width: fit-content; margin: 5px auto;"> <p style="text-align: center;">SEARCH</p> <p style="text-align: center;">T/C : 25</p> </div> <hr/> <p>T/C BIT WRD NUM GO#</p>	<p>- SEARCH</p> <p>- 가 가</p> <p>- SEARCH BOX</p>
<p>[BIT] </p>	<p>EDT 000:RUN-RUN 00001</p> <hr/> <p>STR DIF ▶</p> <div style="border: 1px solid black; padding: 2px; width: fit-content; margin: 5px auto;"> <p style="text-align: center;">SEARCH</p> <p style="text-align: center;">BIT : ■</p> </div>	<p>- BIT F2</p>
<p>[WORD] </p>	<p>EDT 000:RUN-RUN 00001</p> <hr/> <p>STR DIF ▶</p> <div style="border: 1px solid black; padding: 2px; width: fit-content; margin: 5px auto;"> <p style="text-align: center;">SEARCH</p> <p style="text-align: center;">WRD : ■</p> </div>	<p>- WORD F3</p>

[BIT]	R	EDT 000:RUN-RUN 00000	-	BIT
	0	STR DIF	-	5.5
	.	SEARCH		
	Timer	BIT : R000.01		
	1			
[WORD]	R	EDT 000:RUN-RUN 00000	-	WORD
	0	STR DIF	-	5.5
		SEARCH		
		WRD : R0		

[]

T/C Coil

~

SEARCH	EDT 000:RUN-RUN 00000	-	SEARCH KEY	..
	STR DIF			
	SEARCH			
	T/C BIT WRD NUM GO#			
F4	EDT 000:RUN-RUN 00000	-	NUM	F4
	STR DIF			
	SEARCH			
	NUM :			
Timer	EDT 000:RUN-RUN 00000	-		
1	STR DIF			
RST	SEARCH			
>	NUM : 15			
5				

	<pre>SEARCH 00003 LET [FUN-106] D =R0001 S =15 REPEAT SEARCH? [Y/N]</pre>	<p>- 가 - " REPEAT SEARCH? "</p>
	<pre>SEARCH 00003 LET [FUN-106] D =R0001 S =15 NOT FOUND ITEM</pre>	<p>- " REPEAT SEARCH? " YES - " NOT FOUND ITEM "</p>
	<pre>EDT 000:RUN-RUN 00003 LET [FUN-106] D =R0001 \$0000 S =\$000F</pre>	<p>- NO</p>

GO TO STEP

	<pre>EDT 000:RUN-RUN 00000 STR DIF SEARCH T/C BIT WRD NUM GO#</pre>	<p>- SEARCH KEY</p>
	<pre>EDT 000:RUN-RUN 00000 STR DIF SEARCH GO# : ■</pre>	<p>- GO TO STEP F5 - MODE가 Hexa NUM Dec</p>
	<pre>EDT 000:RUN-RUN 00000 STR DIF SEARCH GO# : 15■</pre>	<p>- 가 STEP</p>



```
EDT 000:RUN-RUN 00015
DRLC [FUN-006]
D =R0001 2040
N =1
```

- 가 ..




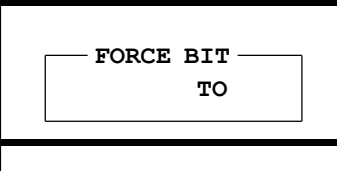
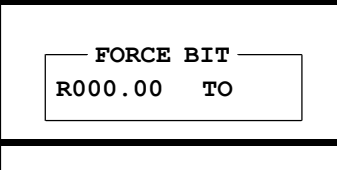
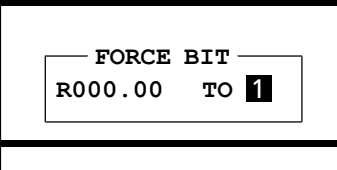
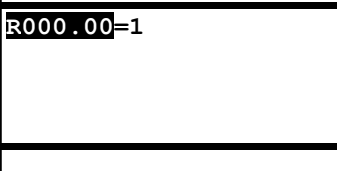
```
EDT 000:RUN-RUN -END-
- END OF PROGRAM -
```

. REPEAT SEARCH?
YES
NOT FOUND ITEM





6.6 (FORCED OUTPUT)

: / / / ALL CLEAR
 : On line




(INSERT)

FORCE	<p>FORCE TABLE</p> 	- FORCE TABLE Display)				
INSERT	or	ENTER Yes	<p>FORCE TABLE</p> <p>FORCE BIT TO</p> 	- bit .		
R	0	.	0	ENTER Yes	<p>FORCE TABLE</p> <p>FORCE BIT R000.00 TO</p> 	- Bit) R000.00 5-3 .
Timer	1	<p>FORCE TABLE</p> <p>FORCE BIT R000.00 TO 1</p> 	- Bit Set or Reset) Set			
ENTER Yes	<p>FORCE TABLE</p> <p>R000.00=1</p> 	-				

(CHANGE)

FORCE	<p>FORCE TABLE</p> <p>R000.00=1 R000.01=0 M000.00=1 M000.01=1 K000.00=1 K000.01=1</p>	<p>· FORCE TABLE Display)</p>	
  	<p>FORCE TABLE</p> <p>R000.00=1 R000.01=0 M000.00=1 M000.01=1 K000.00=1 K000.01=1</p>	<p>· bit) K000.01</p>	
ENTER Yes	<p>FORCE TABLE</p> <p>R000.00=1 R000.01=0 M0 FORCE BIT K0 K000.01 TO 1</p>	<p>) K000.01</p>	
ENTER Yes		<p>FORCE TABLE</p> <p>R000.00=1 R000.01=0 M0 FORCE BIT K0 K000.01 TO 0</p>	<p>) Set Reset</p>
ENTER Yes	<p>FORCE TABLE</p> <p>R000.00=1 R000.01=0 M000.00=1 M000.01=1 K000.00=1 K000.01=0</p>	<p>· FORCE TABLE Display)</p>	

(DELETE)

FORCE	<table border="1"> <tr><th colspan="2">FORCE TABLE</th></tr> <tr><td>R000.00=1</td><td>R000.01=0</td></tr> <tr><td>M000.00=1</td><td>M000.01=1</td></tr> <tr><td>K000.00=1</td><td>K000.01=1</td></tr> <tr><td colspan="2"> </td></tr> </table>	FORCE TABLE		R000.00=1	R000.01=0	M000.00=1	M000.01=1	K000.00=1	K000.01=1			· FORCE TABLE Display)
FORCE TABLE												
R000.00=1	R000.01=0											
M000.00=1	M000.01=1											
K000.00=1	K000.01=1											
  	<table border="1"> <tr><th colspan="2">FORCE TABLE</th></tr> <tr><td>R000.00=1</td><td>R000.01=0</td></tr> <tr><td>M000.00=1</td><td>M000.01=1</td></tr> <tr><td>K000.00=1</td><td>K000.01=1</td></tr> <tr><td colspan="2"> </td></tr> </table>	FORCE TABLE		R000.00=1	R000.01=0	M000.00=1	M000.01=1	K000.00=1	K000.01=1			· bit) K000.01
FORCE TABLE												
R000.00=1	R000.01=0											
M000.00=1	M000.01=1											
K000.00=1	K000.01=1											
DELETE	<table border="1"> <tr><th colspan="2">FORCE TABLE</th></tr> <tr><td>R000.00=1</td><td>R000.01=0</td></tr> <tr><td>M000.00=1</td><td>M000.01=1</td></tr> <tr><td>K000.00=1</td><td> </td></tr> <tr><td colspan="2"> </td></tr> </table>	FORCE TABLE		R000.00=1	R000.01=0	M000.00=1	M000.01=1	K000.00=1				· FORCE TABLE Display)
FORCE TABLE												
R000.00=1	R000.01=0											
M000.00=1	M000.01=1											
K000.00=1												

ALL CLEAR

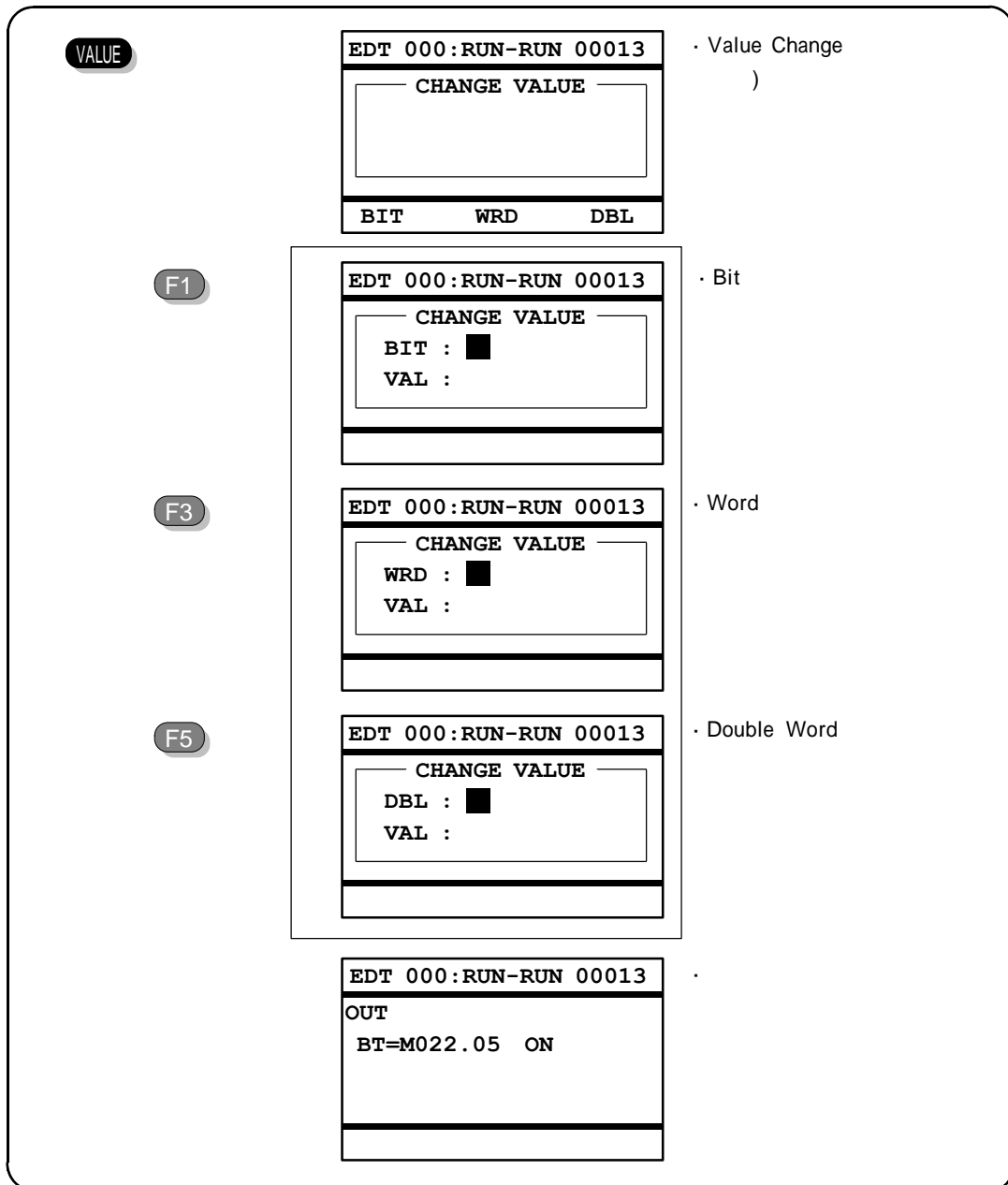
CLEAR	<table border="1"> <tr><th colspan="2">FORCE TABLE</th></tr> <tr><td>R000.00=1</td><td>R000.01=0</td></tr> <tr><td>M000.00=1</td><td>M000.01=1</td></tr> <tr><td>K000.00=1</td><td> </td></tr> <tr><td colspan="2"> </td></tr> <tr><td colspan="2">ALL CLEAR? [Y/N]</td></tr> </table>	FORCE TABLE		R000.00=1	R000.01=0	M000.00=1	M000.01=1	K000.00=1				ALL CLEAR? [Y/N]		· FORCE TABLE Display)
FORCE TABLE														
R000.00=1	R000.01=0													
M000.00=1	M000.01=1													
K000.00=1														
ALL CLEAR? [Y/N]														
ENTER Yes	<table border="1"> <tr><th colspan="2">FORCE TABLE</th></tr> <tr><td> </td><td> </td></tr> <tr><td colspan="2"> </td></tr> </table>	FORCE TABLE						· FORCE TABLE Display)						
FORCE TABLE														

[] FORCE TABLE MODE

ESC

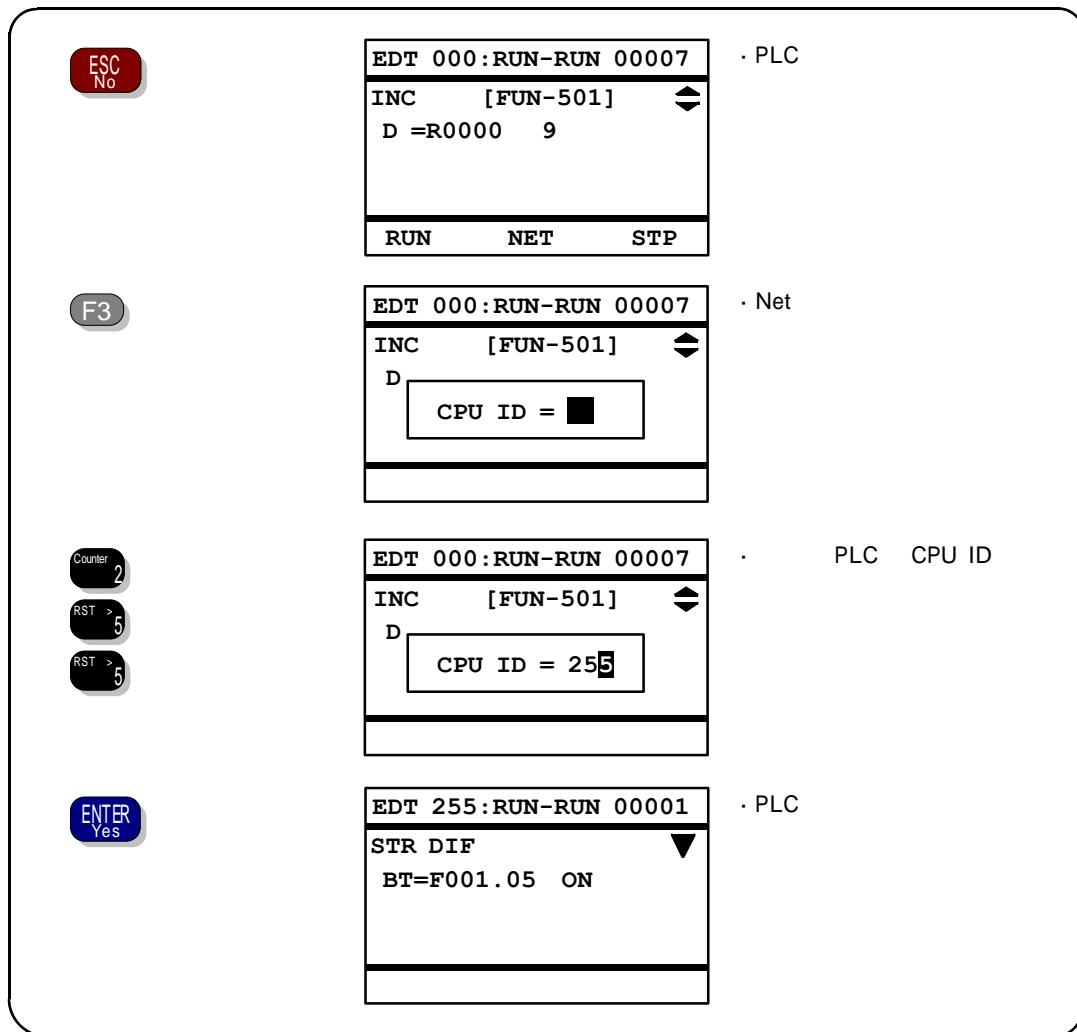
6.7 (VALUE CHANGE)

: / / / ALL CLEAR
 : On line
 [] F1:BIT, F3:WORD, F5:DOUBLE WORD , ESC



[] , , 「5-5」 .

PLC



[] PGM-500

CPU ID 255

PLC
PLC CPU ID

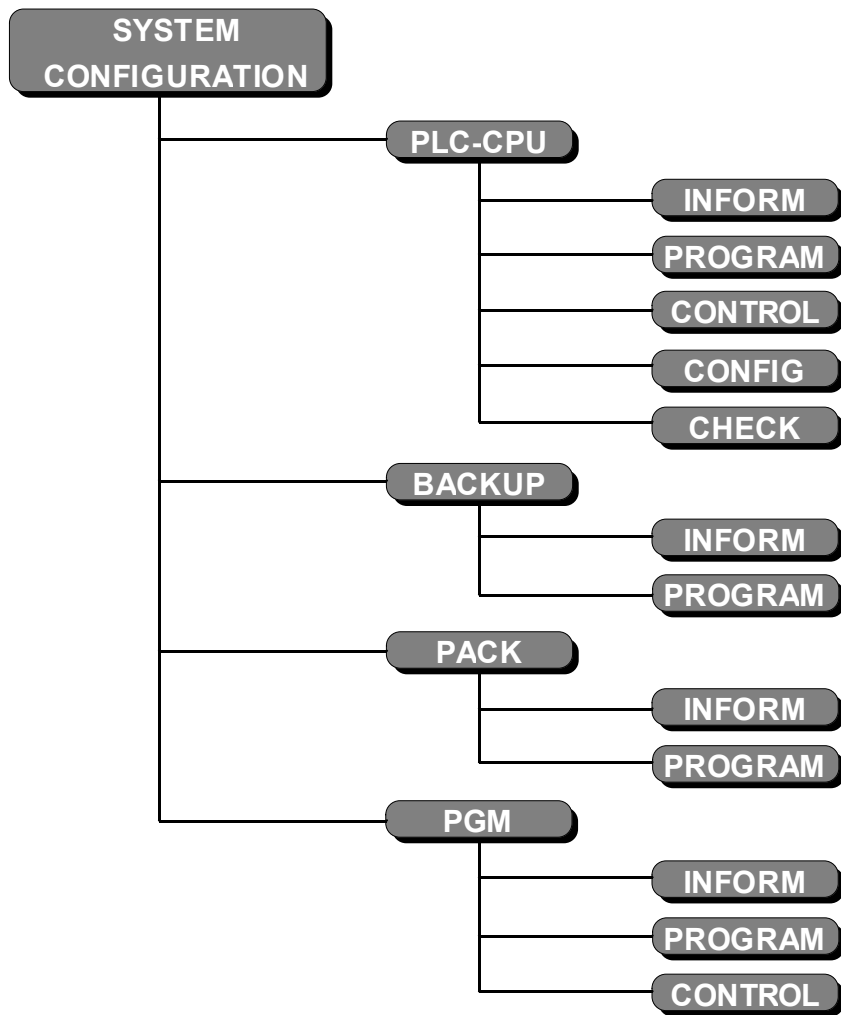
PLC
PLC

7

7.1 PLC	-	(INFORM)
7.2 PLC	-	(PROGRAM)
7.3 PLC	-	(CONTROL)
7.4 PLC	-	(CONFIG)
7.5 PLC	-	(CHECK)
7.6	-	(INFORM)
7.7	-	(PROGRAM)
7.8 PACK	-	(INFORM)
7.9 PACK	-	(PROGRAM)
7.10PGM	-	(INFORM)
7.11PGM	-	(PROGRAM)
7.12PGM	-	(CONTROL)

ESC

ENTER



[7-1] SYSTEM

7.1 PLC - (INFORM)

PLC

가

PLC

PLC	가	PLC
SPC	Series 10, 24S, 100, 120S, 300	
A	Series 200	
N PLUS	Series 70, 700	

CPU

PLC	CPU	PLC
SPC-10	: CPU-14	
SPC-24S	: CPU-24S	
SPC-100	: CPU-10R	
SPC-120S	: CPU-12S	
SPC-300	: CPU-300, CPU-300A, CPU-300B, CPU-300C	
A200	: CPU-200, CPU-201	
N-70PLUS	: CPU-70, CPU-70A	
N-700PLUS	: CPU-700	

ROM

PLC Rom version

SIZE

PLC 가

WORD

CPU PLC

STEP

CPU PLC

WATCHDOG

CPU Watchdog

[] (Watchdog) CPU PLC

SCAN MAX

CPU PLC

SCAN

CPU PLC

[] PLC CPU Run
 , On/Off 1 1

7.2 PLC - (PROGRAM)

PLC PGM-500 Up Load,
 . “ ” 가 PLC Down load , 가 .

MEM => PLC

PGM-500 PLC . (Down Load)

MEM <= PLC

PLC PGM-500 .
 (Up Load).

CLEAR PLC

PLC , PGM-500

7.3 PLC - (CONTROL)

PLC , UPDATE UPDATE,
 ENABLE , TIMER INTERRUPT, KEEP REGISTER
 CLEAR, PROGRAM BACKUP , 가 가

CPU STATUS

PLC RUN/STOP .

IN UPDATE

.

OUT UPDATE

CPU PLC .

OUT ENABLE

.

TIME INTR.

[INT]

KEEP CLEAR

KEEP

PROG BACKUPPLC
PLUS

.「A」 「N

7.4 PLC - (CONFIG)

PLC

CPU IDPLC CPU ID
[] CPU ID**PASSWORD**

PLC

PROGRAM**WATCHDOG**

Watchdog

RTC DATE

Real time clock

RTC TIME

Real time clock

RMF CONF.

. (RMU/RSU .)

I/O CONF.

I/O .(A200 .)

7.5 PLC -

(CHECK)

PLC SYSTEM SYNTAX

ERROR TABLE.

SYSTEM CHECK

가 OK , 가 ERROR 가 ,

SYNTAX CHECK

가 가 OK , 가 ERROR

SYSTEM ERROR

가 , .

CPU STATUS	
ROM SUM CODE	CPU ROM
RAM SUM CODE	CPU RAM
USER MEMORY	(EEPROM)
PROGRAM SYNTAX	PLC
MODULE RANGE	RACK 544
CHANGED MODULE	RACK
ILLEGAL MODULE	RACK
WATCHDOG RANGE	Watchdog
LINK NETWORK	Link
REMOTE NETWORK	
OTHERWISE	

SYNTAX ERROR

SYNTAX CHECK	ERROR
BIT RANGE	가 Bit
T/C RANGE	가
WORD RANGE	가 Word
I/O REFRESH	[INPR/OUTR]
ILLEGAL CODE	
USER MEMORY	(EEPROM) PLC 가
SYSTEM H/W	가
I/O TYPE	
JMP/CALL	[SBR] [JMP] [LBL] [CALL]
LBL NUMBER	(0~63 [LBL] 가 가)
JMPS & JMPE	[JMPS] [JMPE]가
FOR & NEXT	[FOR] [NEXT]가
SBR & RET	[SBR] [RET]가
INT & RETI	[INT] [RETI]가
END INST	[END] 가
OTHERWISE	

7.6 - (INFORM)

PGM-500

PLC

PLC

PLC

PLC

CPU

PLC

PLC CPU

WORD

STEP**NAME****7.7 - (PROGRAM)**

PLC PGM-500 Up load
PLC

MEM => BACKUP

PGM-500 PLC
[]

MEM <= BACKUP

PLC PGM-500
[]
「BACKUP MEMORY EMPTY」

CLEAR BACKUP**7.8 PACK - (INFORM)**

PACK PLC

PLC

PACK PLC

CPU

PACK PLC CPU

WORD

PACK

STEP

PACK

NAME

PACK

7.9 PACK - (PROGRAM)

PACK, PLC, PGM-500, Up load
 PACK, PLC, ..

MEM => PACK

PGM-500, PACK
 []

MEM <= PACK

PACK, PGM-500, PLC
 [] PACK
 「BACKUP MEMORY EMPTY」

CLEAR PACK

PACK, PLC

7.10 PGM - (INFORM)

PGM-500, PLC
 PLC

PLC

PLC, PLC

CPU

PLC, CPU, PLC
 CPU

WORD

PGM-500, PLC

STEP

PGM-500

PLC

STEP

NAME

PGM-500

PLC

7.11 PGM - (PROGRAM)**CLEAR MEMORY**

PGM-500

PLC

I/O CONFIG

I/O

7.12 PGM - (CONTROL)**BAUD RATE****BACKLIGHT**

BACK LIGHT ON/OFF

BEEP SOUND

BEEP SOUND ON/OFF

POWER SAVE

Power Save

1

30

. '0'

가

POWER SAVE

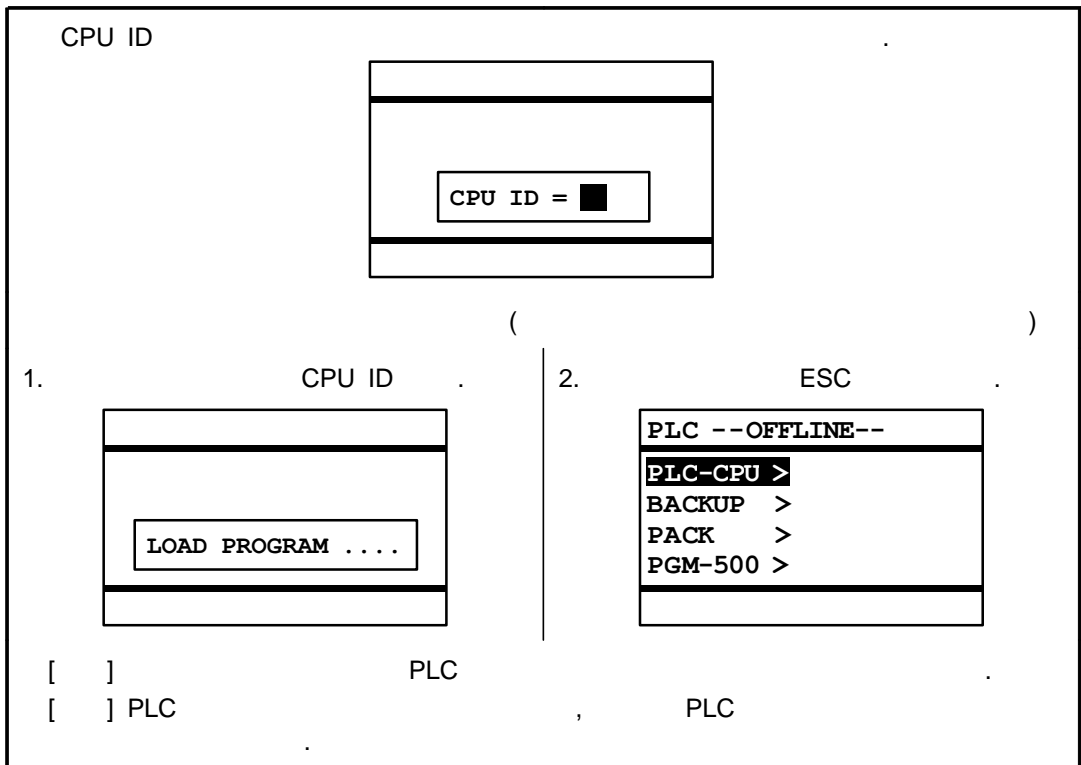
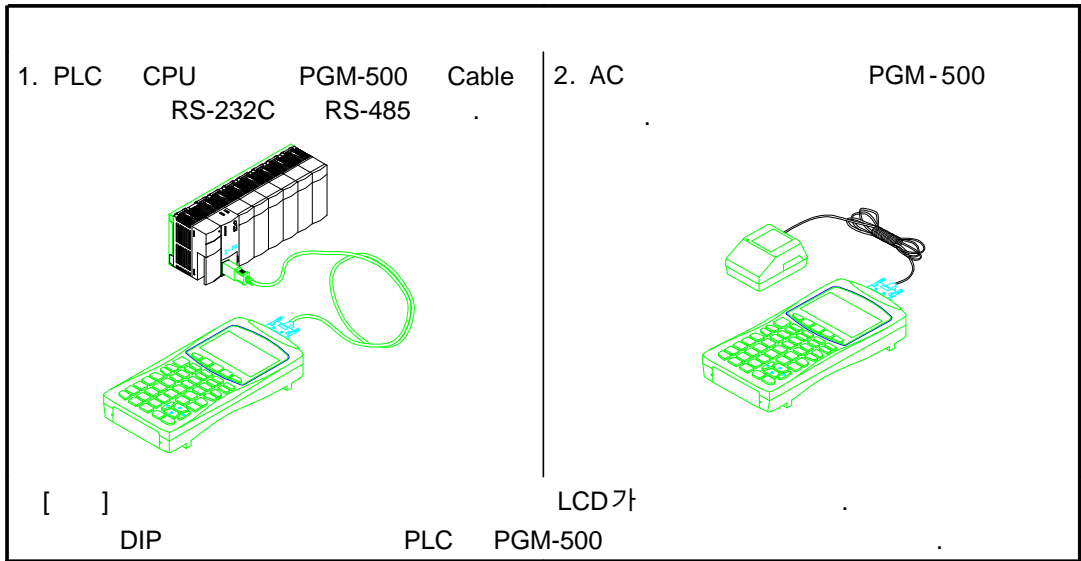
8

8.1

-
-
- On-line
- Off-line
- (Down Load)
- (Up Load)
- (Backup)
- (Pack)

8.2

8.1



[] PGM-500
PLC RUN

1. ON-LINE

EDT 255:RUN-RUN 00000

STR DIF

BT=F001.05 ON

2. PLC

[6.9] " PLC

EDT 255:RUN-RUN 00000

STR DIF ▼

B

CPU ID = ■

EDT 000:RUN-RUN 00000

STR DIF ▼

BT=F001.05 ON

[] RS-485 가

[] / 가/ / / / / 6

3. OFF-LINE

PLC --OFFLINE--

PLC-CPU >

BACKUP >

PACK >

PGM-500 >

PLC CPU ID

PLC --OFFLINE--

PLC-CPU >

BA

CPU ID = ■

PA

PG

PLC 255:RUN-RUN

PLC-CPU >

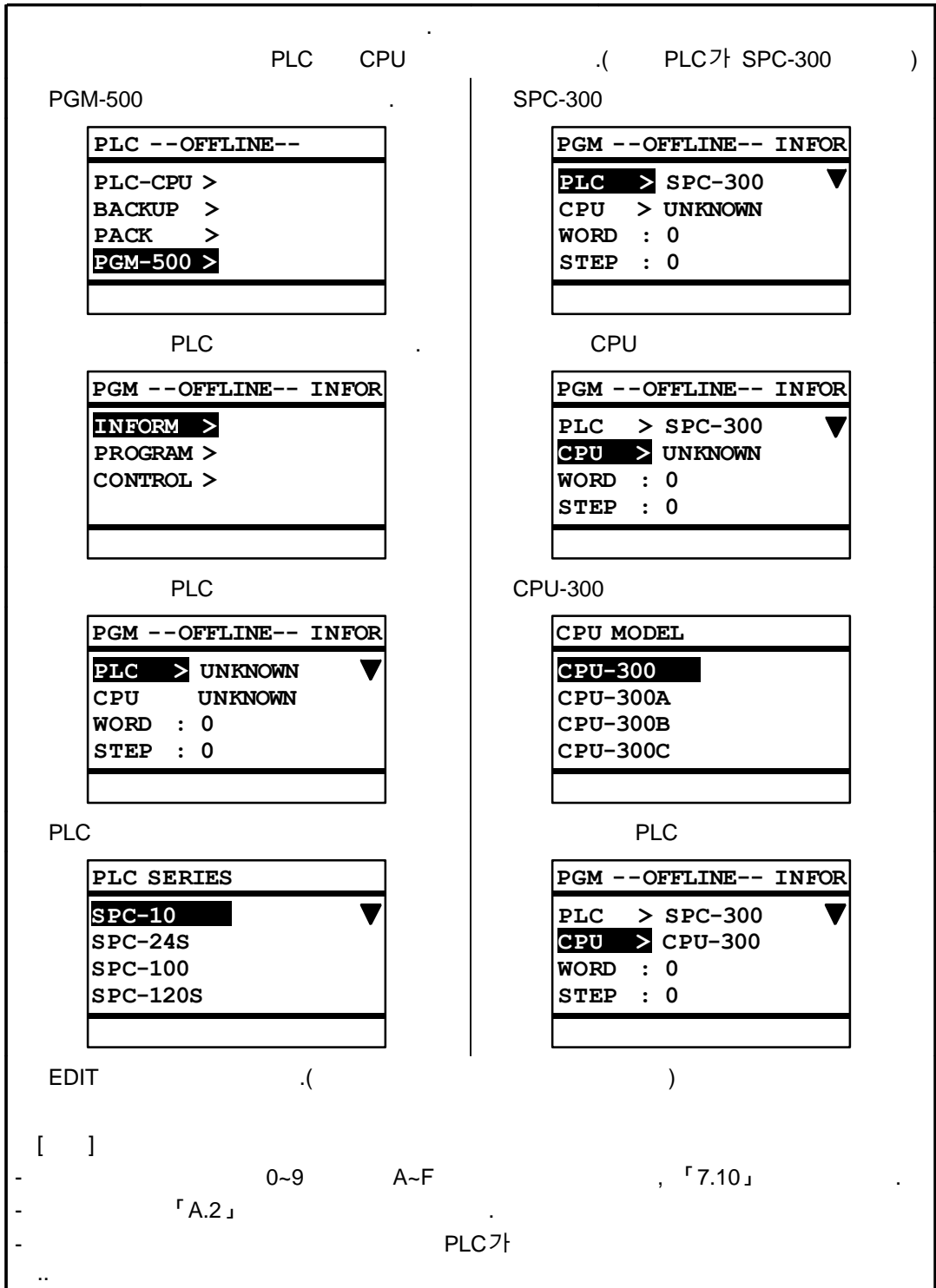
BACKUP >

PACK >

PGM-500 >

[] PGM-500 UP-

LOAD



(DOWN LOAD)

Down Load ?

PGM-500 PLC (Writing)

[] Backup Pack PLC

PGM-500 가 가 , CPU ID PLC

ESC+F3 PLC CPU . ()

PLC --OFFLINE--

PLC-CPU >

BA

PA CPU ID = ■

PG

PLC 255:RUN-RUN

PLC-CPU >

BACKUP >

PACK >

PGM-500 >

PROGRAM

PLC 255:RUN-RUN INFOR

INFORM >

PROGRAM >

CONTROL >

CONFIG >

DOWN LOAD (MEM PLC)

PLC 255:RUN-RUN PROGR

MEM => PLC >

MEM <= PLC >

CLEAR PLC >

PGM-500 PLC

DOWN LOAD

PLC 255:RUN-RUN PROGR

MEM => PLC >

WRITE PROGRAM

EDT MODE

EDT 255:RUN-RUN 00000

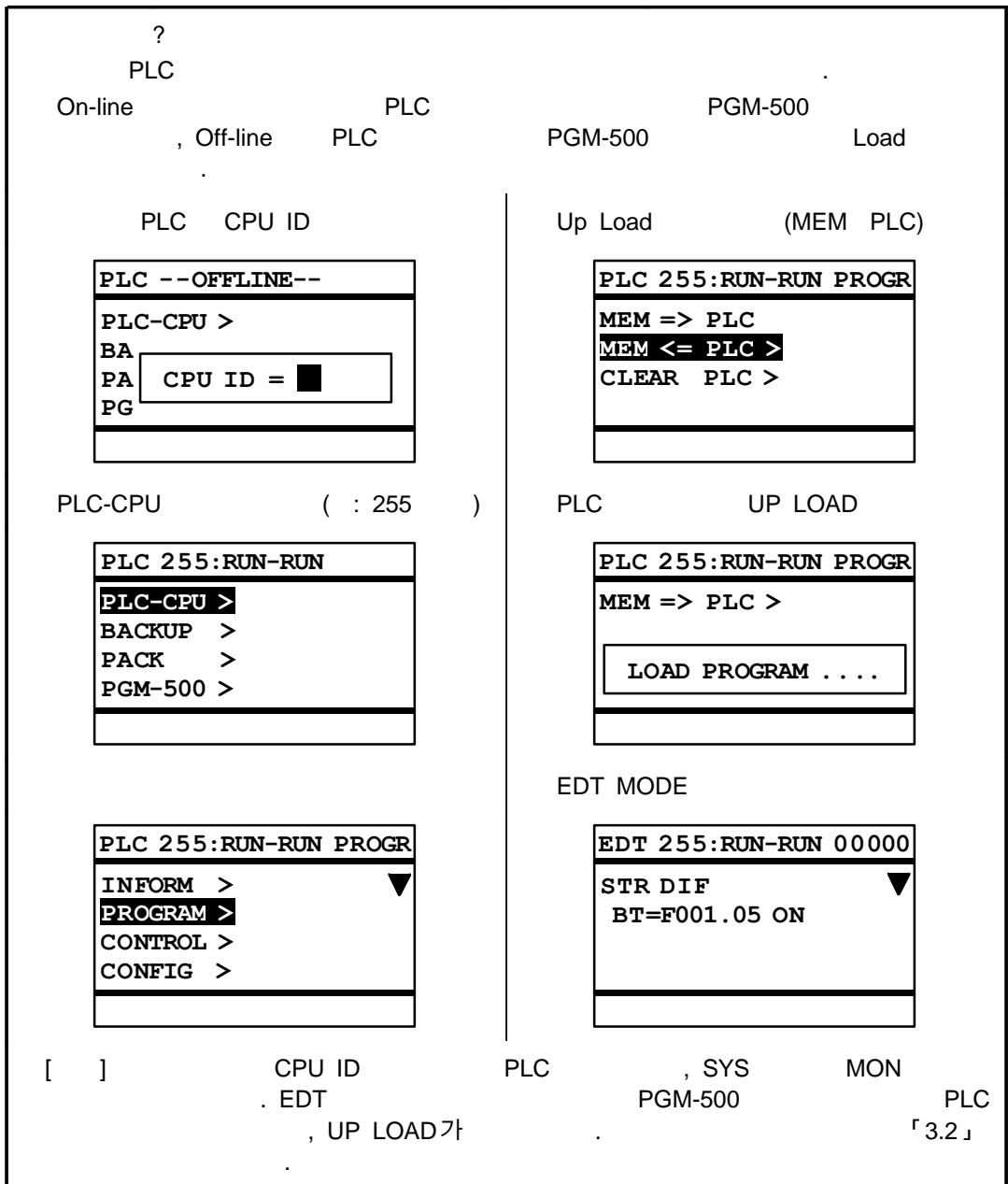
STR DIF ▼

BT=F001.05 ON

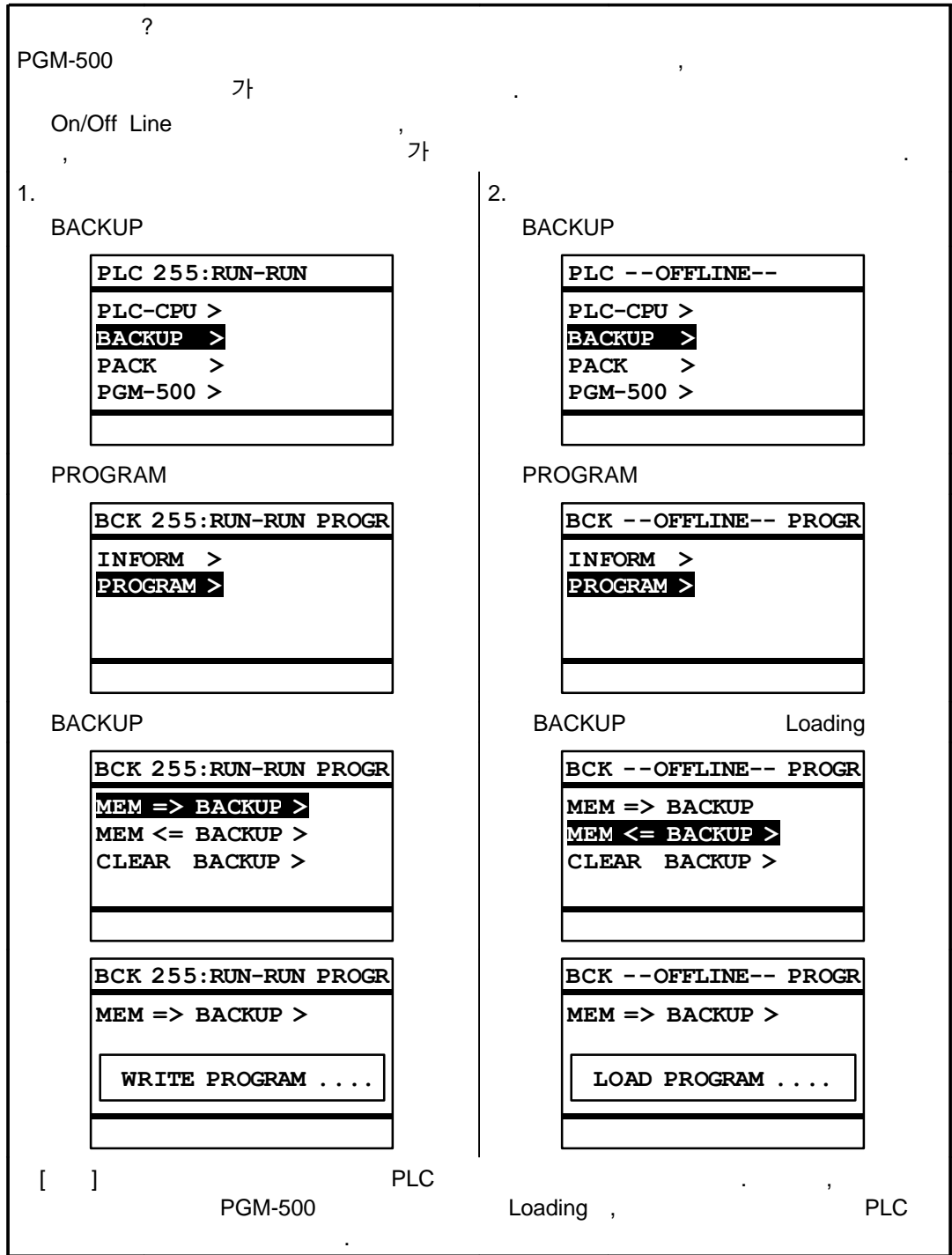
[] Backup Pack Flash

가 「BACKUP」, 「FLASH」, 「8.2」

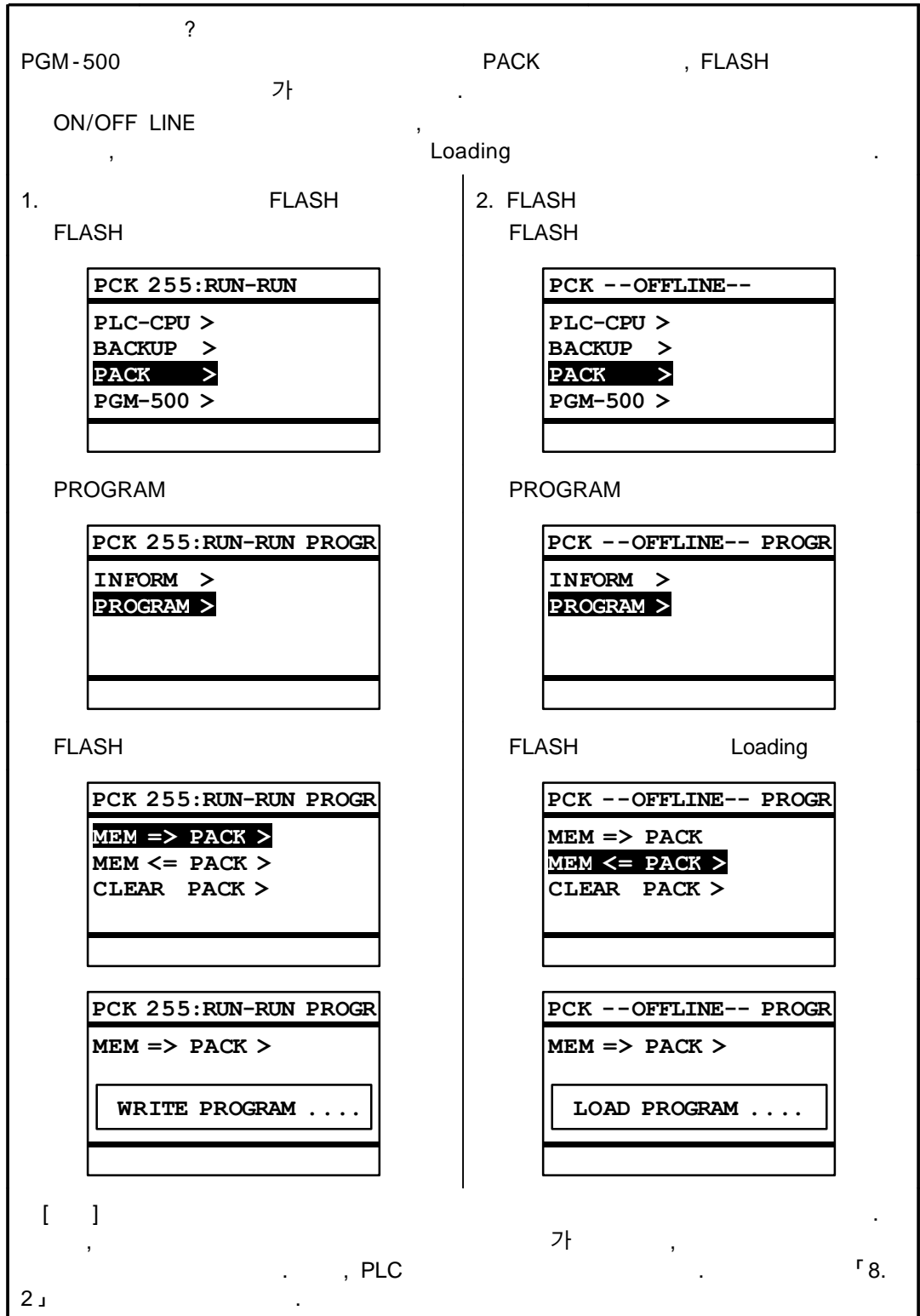
(UP LOAD)



(BACKUP)



(PACK)



8-2

PLC

1. PLC
2. CPU ID
3. SYSTEM
4. BACKUP PROGRAM
5. MEM BACKUP

PLC

1. PLC
2. CPU ID
3. SYSTEM
4. PACK PROGRAM
5. MEM = PACK

PLC

1. PLC
2. SYSTEM (OFF LINE)
3. BACKUP
4. MEM = BACKUP
5. SYSTEM PLC (ESC+F3)
6. PLC-CPU PROGRAM
7. MEM = PLC

1. PLC
2. SYSTEM
3. BACKUP PROGRAM
4. MEM = BACKUP
5. PACK PROGRAM
6. MEM = PACK

PLC

1. PLC
2. SYSTEM (OFF LINE)
3. PACK
4. MEM = PACK
5. SYSTEM PLC (ESC+F3)
6. PLC-CPU PROGRAM

7. MEM = PLC

1. PLC
2. SYSTEM
3. PACK PROGRAM
4. MEM = PACK
5. BACKUP PROGRAM
6. MEM = BACKUP



A.1

A.2

APPENDIX

A.1

[] PLC 가 가 , 가 .

SYSTEM HARDWARE ERROR	PGM-500	A/S
DRIVER LOAD ERROR	Loading	A/S
ILLEGAL DIP SETTINGS	DIP 가	2.2
TARGET SERIES CHANGED	PGM-500 PLC PLC 가	
INTERNAL MEMORY ERROR	PGM-500	A/S
UNKNOWN PLC SERIES	PLC PGM-500	가

DISCONNECT .. RETRY		-
ILLEGAL COMMAND	()	PLC
RANGE OVER	PLC RESOURCE	PLC
ILLEGAL INST.	가 ()	PLC
CPU [RUN STOP]	RUN 가	PLC
TOO LONG FRAME		PLC
MEMORY WRITING ERROR	PLC ()	PLC
NO QUERY	Query	PLC
NOT FOUND FOR SEARCH	SERARCH	-

NOT FOUND ITEM	가	-
NOT FOUND FLASH-MEM	FLASH 가	-
FLASH-MEM WRITE ERROR	FLASH	-
PROGRAM EMPTY		-
CONFIG RANGE OVER		-

APPENDIX
























CPU MODULE MISMATCHED	CPU	PLC
UNUSABLE CPU ID	가 CPU ID	0~255
NOT FOUND CPU MODULE	PLC	DIP s/w
PASSWORD MISMATCHED	가	-
ILLEGAL REMOTE CONFIG		-
ILLEGAL I/O CONFIG	I/O	-

NOT FOUND INST.	가	-
UNUSABLE INST.	가	-
ILLEGAL INST.		-
PROGRAM OVERFLOW		-
TOO MANY SR INST.	SR 가 256	-
ILLEGAL FUN CODE		-













ILLEGAL CHARACTER	가	-
WORD REGS. EXPECTED	가	-
WORD RANGE OVER		-
UNUSABLE WORD REGS.	가	-
BIT REGS. EXPECTED	가	-
BIT RANGE OVER		-
UNUSABLE BIT REGS.	가	-
NUMBER/WORD EXPECTED		-
NUMBER EXPECTED		-
NUMBER RANGE OVER		-







APPENDIX











A.2

[EDT MON SYS]	EDT MON , PGM EDT SYS , PGM MON SYS	    	3.2
PLC RUN/STOP [EDT MON]	PLC RUN PLC STOP	   	6.9 7.12
PLC NETWORK [EDT MON SYS]	PLC-CPU	 	6.9 7.12
STEP [EDT]		 	3.2
[EDT]			6.1
가 [EDT]	가		6.1
[EDT]			6.3
[EDT]			6.2
[EDT MON]	DEC. HEXA. ,		3.2
[EDT]	PGM		6.4
[EDT MON]	, STEP		6.5
[EDT MON]	(SET/RESET)		6.6
[EDT MON]			6.7
SCAN RUN [EDT MON]	PLC 가 PAUSE or REMOTE		6.8

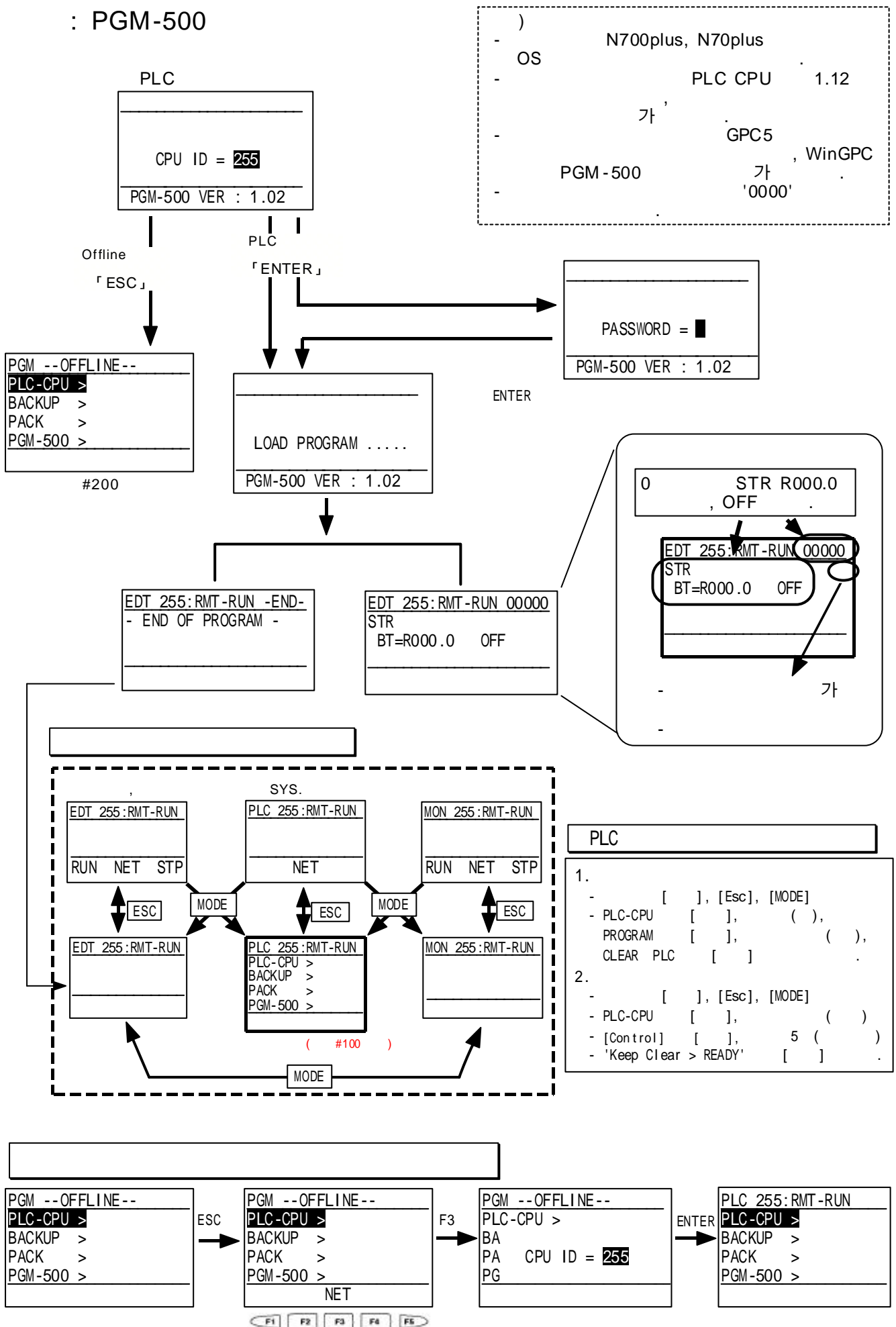
APPENDIX

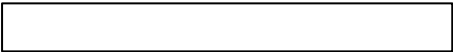
[EDT] [SYS]	EDT SYS	 	3.1, 8.1
PLC NETWORK [EDT MON SYS]	PLC-CPU	 	8.1
STEP [EDT]]			3.2
[EDT]]			6.1
[EDT] 가]	가		6.1
[EDT]]			6.3
[EDT]]			6.2
[EDT MON]]	DEC. HEXA.		3.2
[EDT]]	PGM		6.4
[EDT MON]]	STEP		6.5

BIT REGS. [EDT MON]]	BIT	 	6.7
WORD REGS. [EDT MON]]	WORD	 	6.7
DWORD REGS. [EDT MON]]	DOUBLE WORD	 	6.7

T/C CHANNEL [EDT MON]]	T/C CHANNEL	 	6.5
BIT REGS. [EDT MON]]	BIT	 	6.5
WORD REGS. [EDT MON]]	WORD	 	6.5
NUMBER [EDT MON]]		 	6.5
GO TO STEP [EDT MON]]	STEP	 	6.5

: PGM-500





:#100

PLC 255:RMT-RUN PLC-CPU > BACKUP > PACK > PGM-500 >	PLC 255:RMT-RUN INFOR INFORM > PROGRAM > CONTROL > CONFIG > CHECK >	PLC 255:RMT-RUN INFOR PLC : N-70PLUS CPU : CPL9215A ROM : 1.12 SIZE : 9600 WORD : 0 STEP : 0 WATCHDOG : 3000 SCAN MAX : 6 SCAN : 3	*** PLC PLC CPU OS ()
	PLC 255:RMT-RUN PROGR INFORM > PROGRAM > CONTROL > CONFIG > CHECK >	PLC 255:RMT-RUN PROGR MEM => PLC MEM <= PLC CLEAR PLC	PGM500 PLC PLC (1). PLC
	PLC 255:RMT-RUN CONTR INFORM > PROGRAM > CONTROL > CONFIG > CHECK >	PLC 255:RMT-RUN CONTR CPU STATUS > RUN IN UPDATE > YES OUT UPDATE > YES OUT ENABLE > YES TIME INTR. > NO KEEP CLEAR > READY PROG BACKUP > READY	CPU RUN STOP Updata () (K,W) 0 EEPROM (Flash ROM)
	PLC 255:RMT-RUN CONFG INFORM > PROGRAM > CONTROL > CONFIG > CHECK >	PLC 255:RMT-RUN CONFG CPU ID > 255 PASSWORD > #### PROGRAM > TEST WATCHDOG > 3000 RTC DATE > 99-11-11 RTC TIME > 09:09:09 RMT CONF > NONE I/O CONF > NONE	CPU ID (:0-223, :255) (PGM 1.01 가 , :0000) 가 (/ /) 가 (/ /)
	PLC 255:RMT-RUN CHECK PROGRAM > CONTROL > CONFIG > CHECK >	PLC 255:RMT-RUN CHECK SYSTEM CHECK > OK SYNTAX CHECK > OK SYSTEM ERROR : NONE SYNTAX ERROR : NONE	()
BCK 255:RMT-RUN PLC-CPU > BACKUP > PACK > PGM-500 >	BCK 255:RMT-RUN PROGR INFORM > PROGRAM >	BCK 255:RMT-RUN INFOR PLC : SPC-300 CPU : CPU-300 WORD : STEP : NAME :	PLC (N plus SPC-300) CPU (N plus SPC-300)
	BCK 255:RMT-RUN INFOR INFORM > PROGRAM >	BCK 255:RMT-RUN INFOR MEM => BACKUP > MEM <= BACKUP > CLEAR BACKUP >	PLC
PCK 255:RMT-RUN PLC-CPU > BACKUP > PACK > PGM-500 >	PCK 255:RMT-RUN INFOR INFORM > PROGRAM >	PCK 255:RMT-RUN INFOR NOT FOUND FLASH-MEM	PACK PGM500 PGM-WR
	PCK 255:RMT-RUN PROGR INFORM > PROGRAM >	PCK 255:RMT-RUN PROGR MEM => PACK > MEM <= PACK > CLEAR PACK >	** PACK PGM500 PGM-WR PGM-500 PGM-500 RAM
PGM 255:RMT-RUN PLC-CPU > BACKUP > PACK > PGM-500 >	PGM 255:RMT-RUN INFOR INFORM > PROGRAM > CONTROL >	PGM 255:RMT-RUN INFOR PLC > N-70PLUS CPU > CPL9215A WORD : 10 STEP : 10 NAME > TEST	PGM-500 PLC PGM-500 CPU PGM-500 PGM-500 PGM-500
	PGM 255:RMT-RUN PROGR INFORM > PROGRAM > CONTROL >	PGM 255:RMT-RUN PROGR CLEAR MEMORY > I/O CONFIG :	PGM-500 PGM-500 I/O
	PGM 255:RMT-RUN CONTR INFORM > PROGRAM > CONTROL >	PGM 255:RMT-RUN CONTR BAUD RATE : 9600 BACK LIGHT > OFF BEEP SOUND > OFF POWER SAVE > 0 MIN.	PGM-500 (DIP ON OFF) ON/OFF

[Empty box]

:#200

PGM --OFFLINE-- PLC-CPU > BACKUP > PACK > PGM-500 >	PLC --OFFLINE-- INFOR INFORM > PROGRAM > CONTROL > CONFIG > CHECK >	PLC --OFFLINE-- INFOR PLC : CPU : ROM : SIZE : WORD : STEP : WATCHDOG : SCAN MAX : SCAN :	
	PLC --OFFLINE-- PROGR INFORM > PROGRAM > CONTROL > CONFIG > CHECK >	PLC --OFFLINE-- PROGR MEM => PLC MEM <= PLC CLEAR PLC	PGM500 PLC PLC PLC (1).
	PLC --OFFLINE-- CONTR INFORM > PROGRAM > CONTROL > CONFIG > CHECK >	PLC --OFFLINE-- CONTR CPU STATUS IN UPDATE OUT UPDATE OUT ENABLE TIME INTR. KEEP CLEAR PROG BACKUP	
	PLC --OFFLINE-- CONFG INFORM > PROGRAM > CONTROL > CONFIG > CHECK >	PLC --OFFLINE-- CONFG CPU ID PASSWORD PROGRAM WATCHDOG RTC DATE RTC TIME RMT CONF I/O CONF	
	PLC --OFFLINE-- CHECK PROGRAM > CONTROL > CONFIG > CHECK >	PLC --OFFLINE-- CHECK SYSTEM CHECK SYNTAX CHECK SYSTEM ERROR SYNTAX ERROR	
BAK --OFFLINE-- PLC-CPU > BACKUP > PACK > PGM-500 >	BAK --OFFLINE-- PROGR INFORM > PROGRAM >	BAK --OFFLINE-- INFOR PLC : CPU : WORD : STEP : NAME :	PLC (N plus SPC-300) CPU (N plus SPC-300)
	BAK --OFFLINE-- INFOR INFORM > PROGRAM >	BAK --OFFLINE-- INFOR MEM => BACKUP > MEM <= BACKUP > CLEAR BACKUP >	PLC
PCK --OFFLINE-- PLC-CPU > BACKUP > PACK > PGM-500 >	PCK --OFFLINE-- INFOR INFORM > PROGRAM >	PCK --OFFLINE-- INFOR	PACK PGM500 PGM-WR
	PCK --OFFLINE-- PROGR INFORM > PROGRAM >	PCK --OFFLINE-- PROGR MEM => PACK > MEM <= PACK > CLEAR PACK >	** PACK PGM500 PGM-WR PGM-500 PGM-500 RAM
PGM --OFFLINE-- PLC-CPU > BACKUP > PACK > PGM-500 >	PGM --OFFLINE-- INFOR INFORM > PROGRAM > CONTROL >	PGM --OFFLINE-- INFOR PLC > CPU > WORD : STEP : NAME >	PGM-500 PLC PGM-500 CPU PGM-500 PGM-500 PGM-500
	PGM --OFFLINE-- PROGR INFORM > PROGRAM > CONTROL >	PGM --OFFLINE-- PROGR CLEAR MEMORY > I/O CONFIG :	PGM-500 PGM-500 I/O
	PGM --OFFLINE-- CONTR INFORM > PROGRAM > CONTROL >	PGM --OFFLINE-- CONTR BAUD RATE : BACK LIGHT > BEEP SOUND > POWER SAVE >	PGM-500 (DIP ON OFF) ON/OFF

[Empty box]

```

PGM --OFFLINE--
PLC-CPU >
BACKUP >
PACK >
PGM-500 >

```

ESC

```

PGM --OFFLINE--
PLC-CPU >
BACKUP >
PACK >
PGM-500 >
NET

```

F3

```

PGM --OFFLINE--
PLC-CPU >
BA
PA CPU ID = 255
PG

```

ENTER

```

PLC 255:RMT-RUN
PLC-CPU >
BACKUP >
PACK >
PGM-500 >

```

